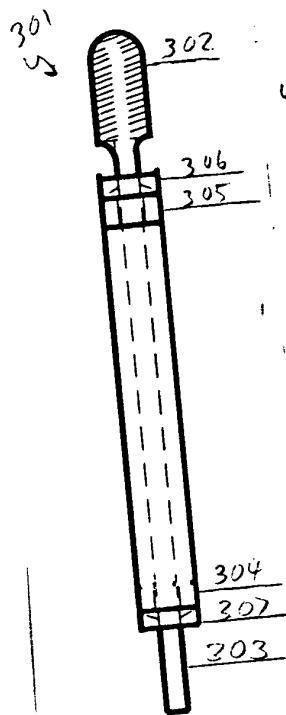
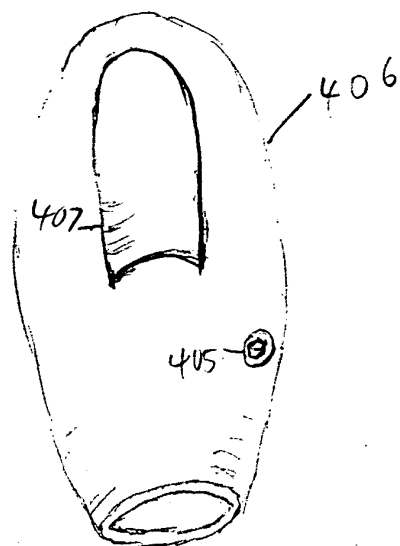


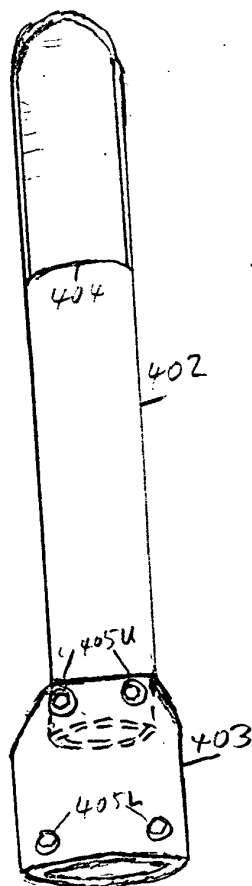
A



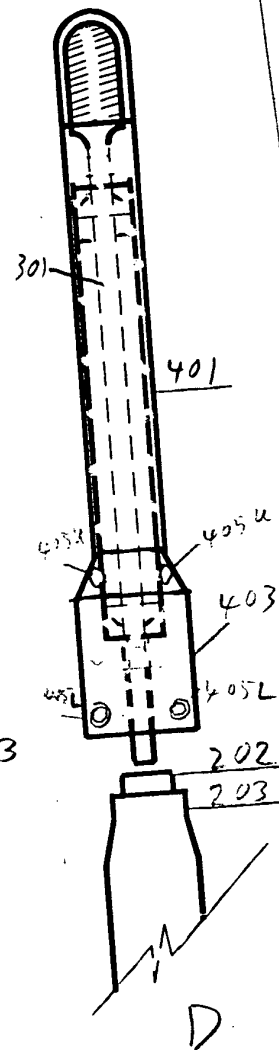
B



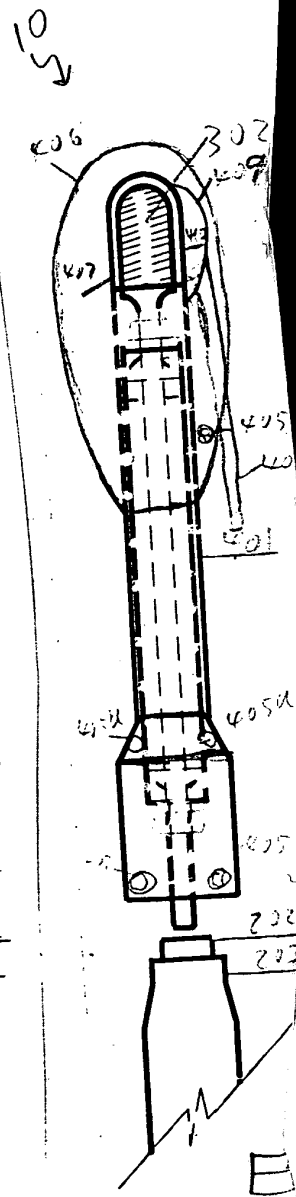
401



C



D



E

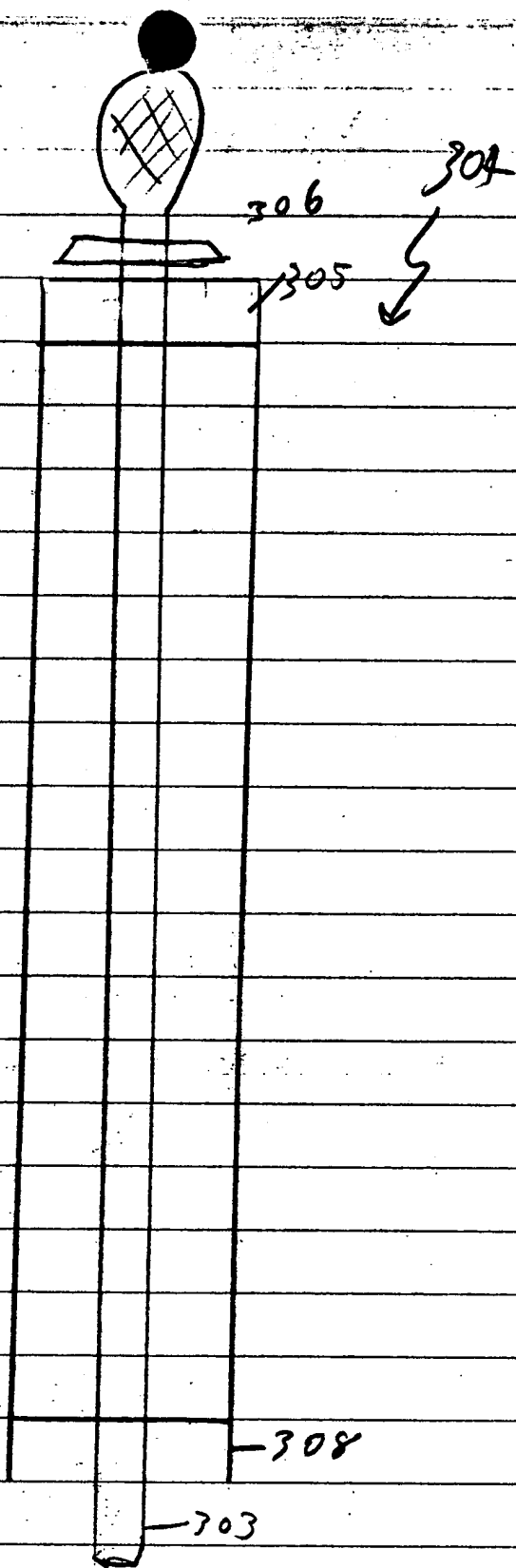


Fig 2

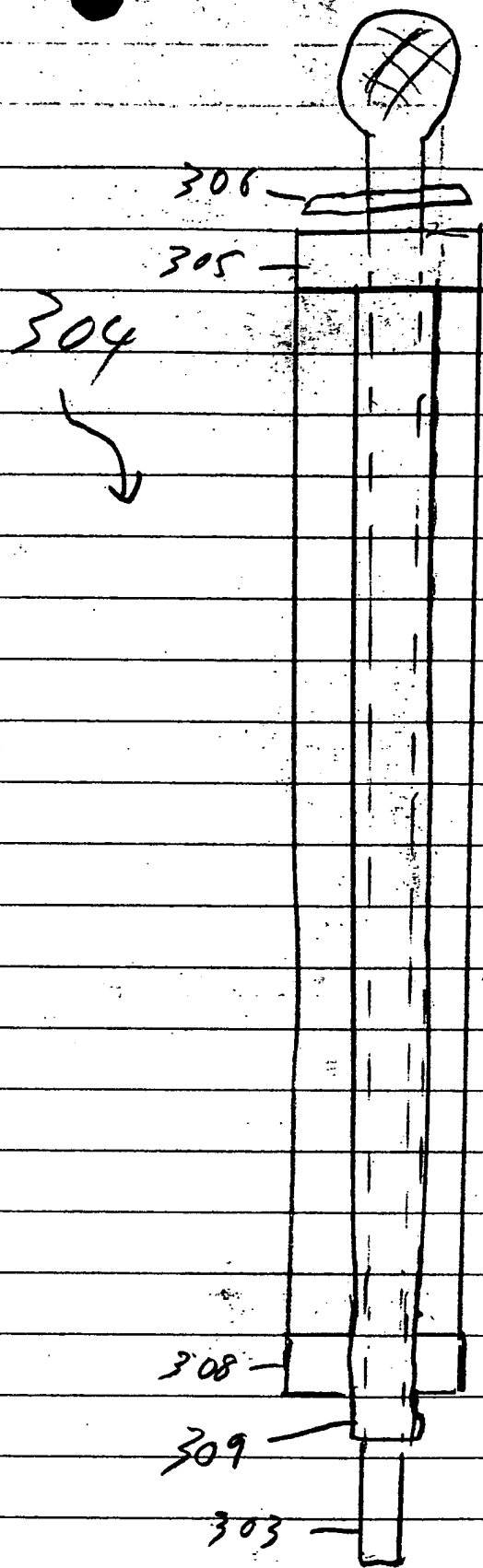
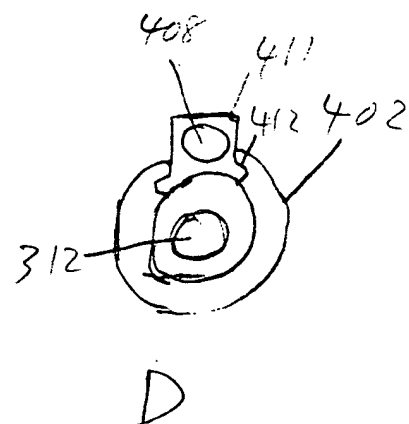
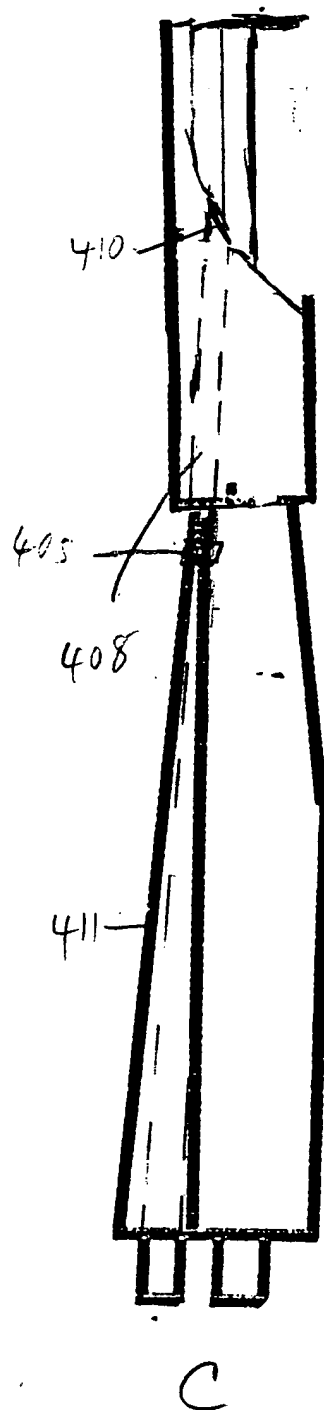
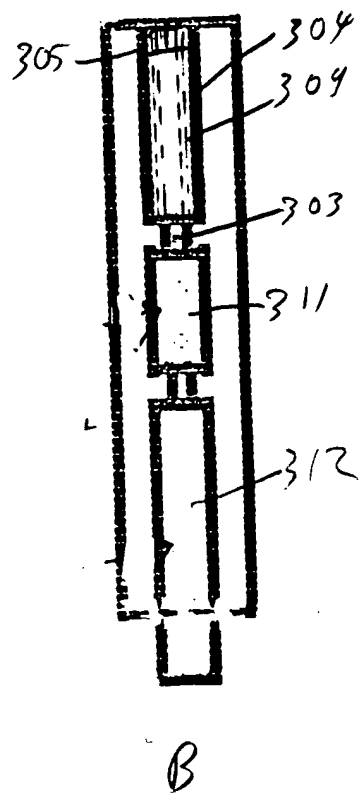
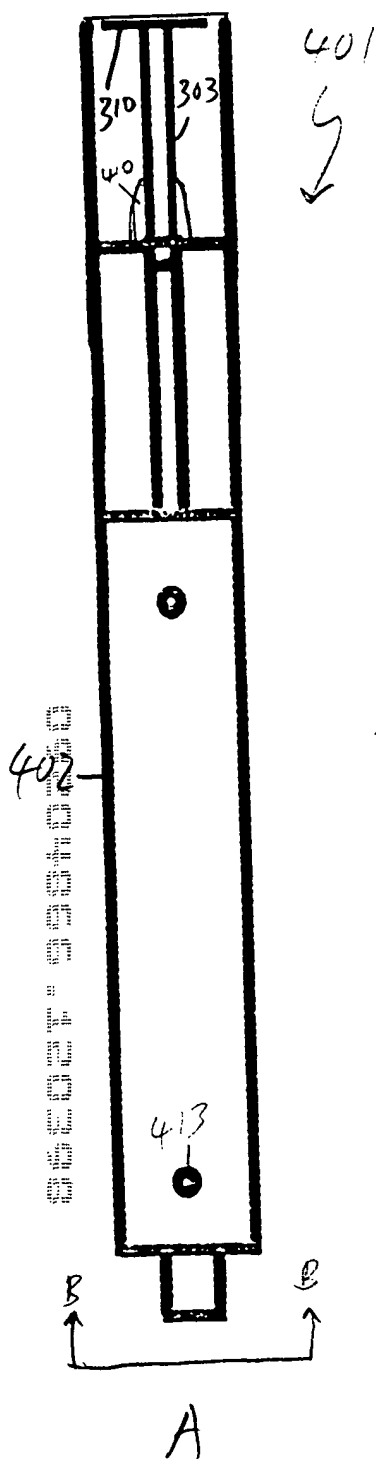
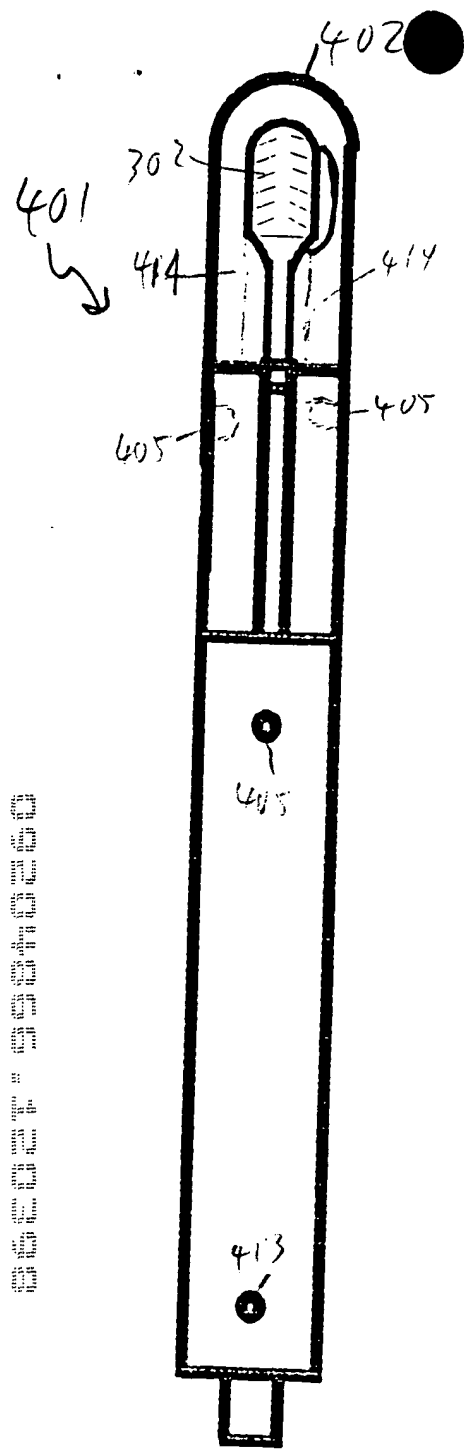


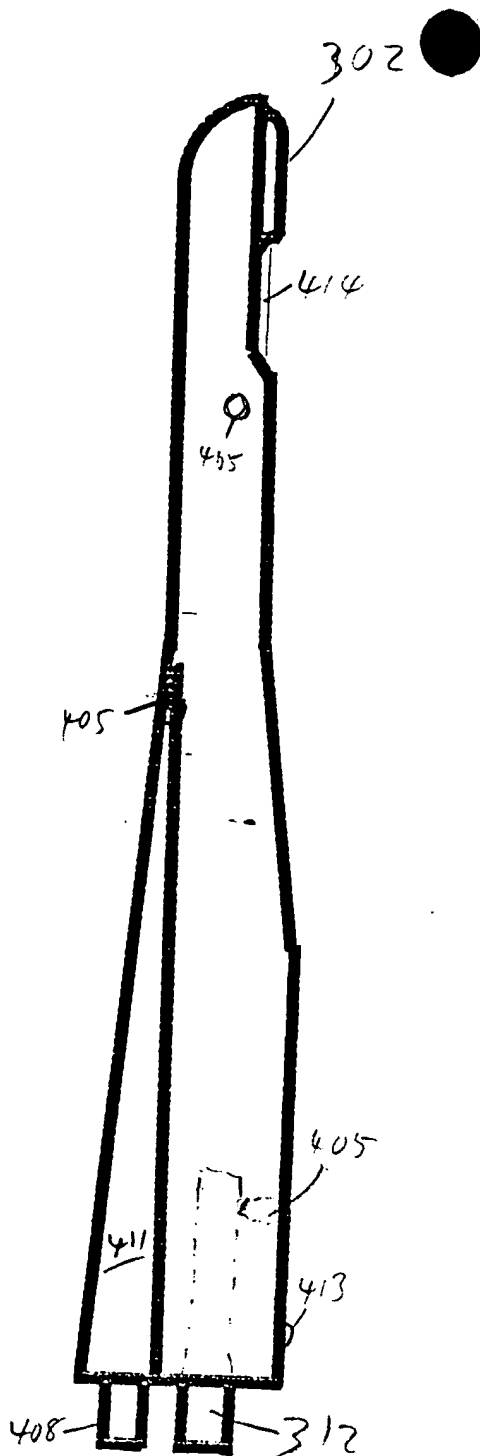
Fig 3



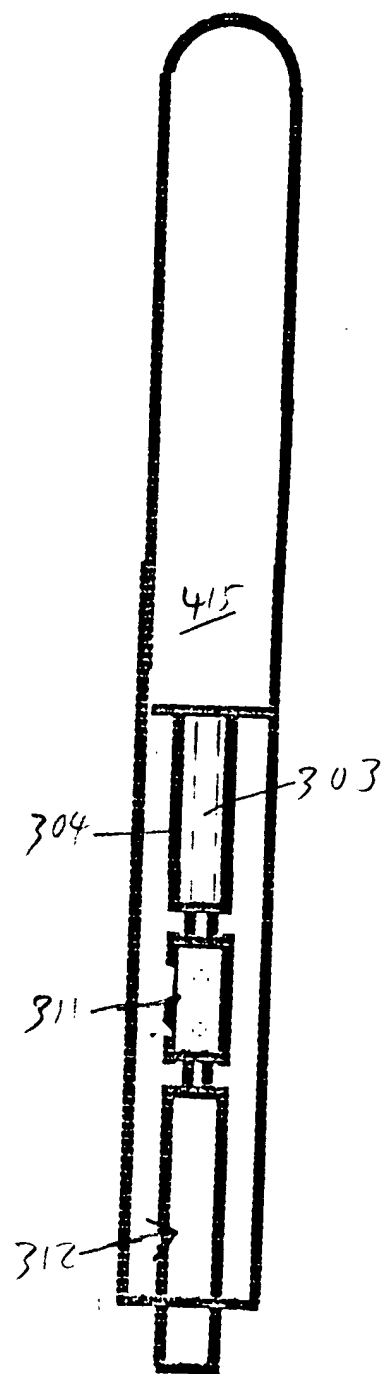
F1-4



A

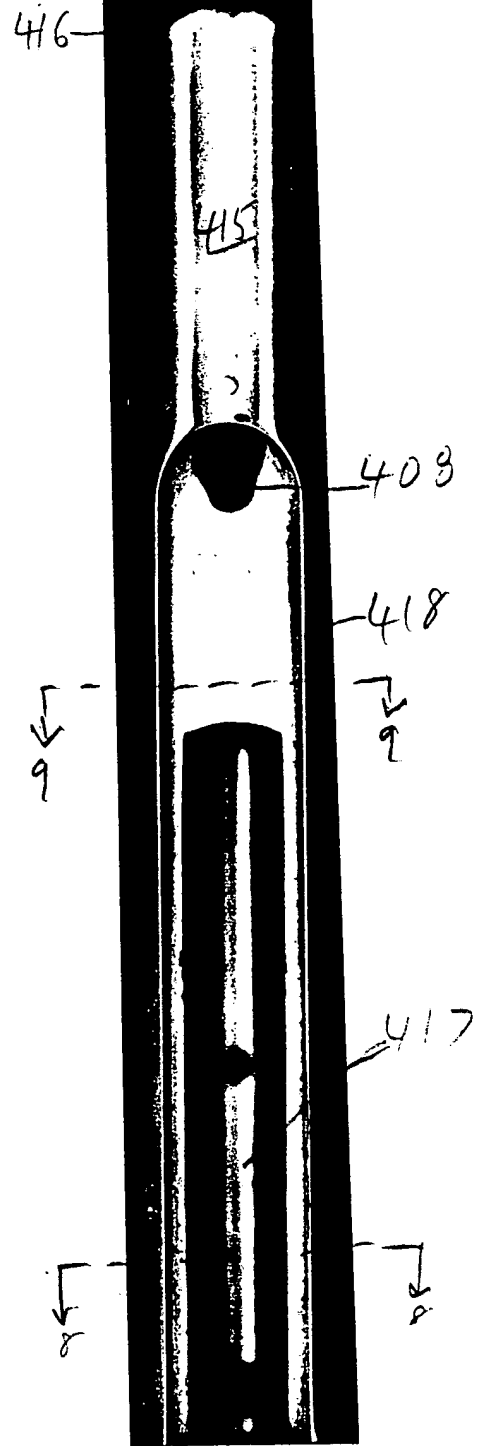
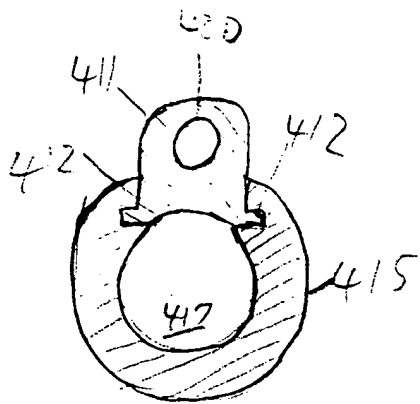
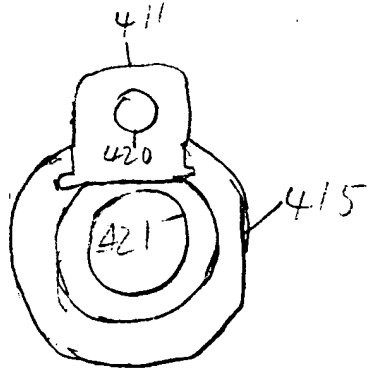
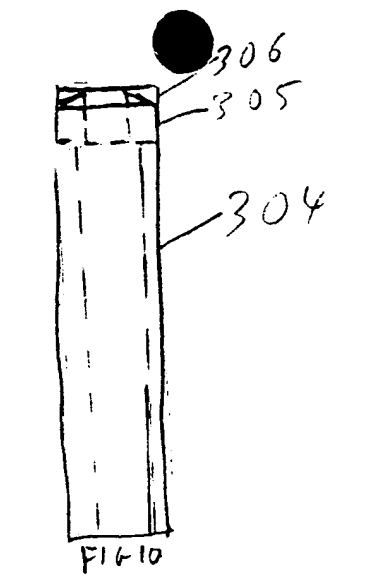


B



C

FIG 5



SECRET

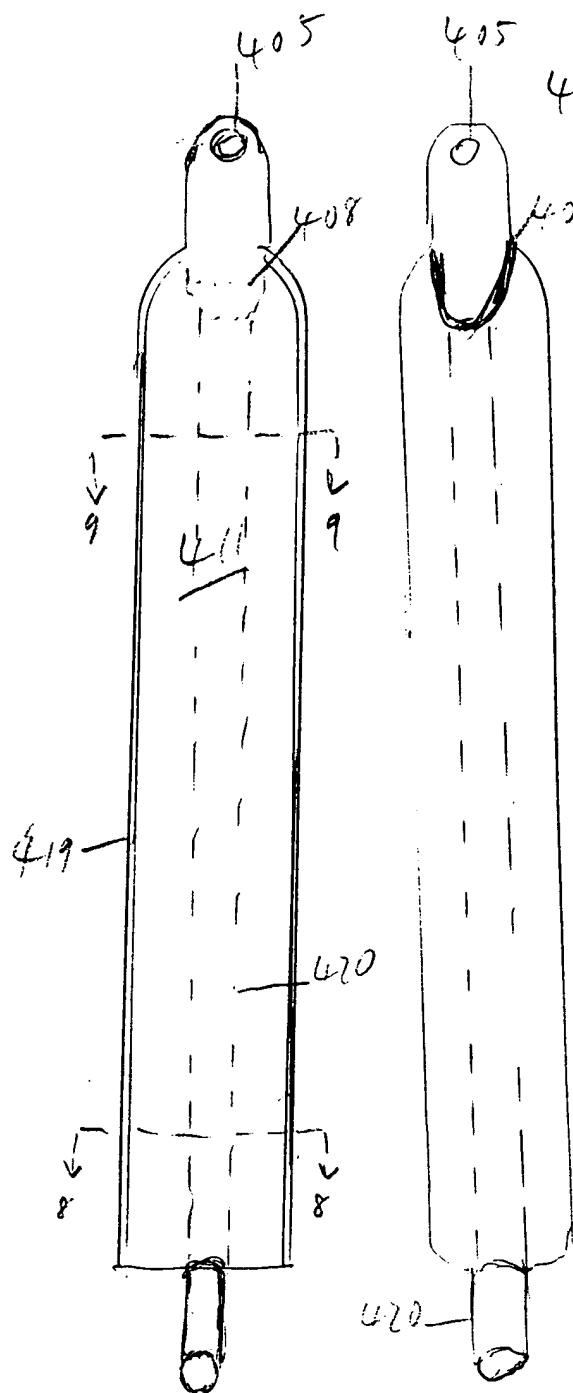


FIG 7F

7B

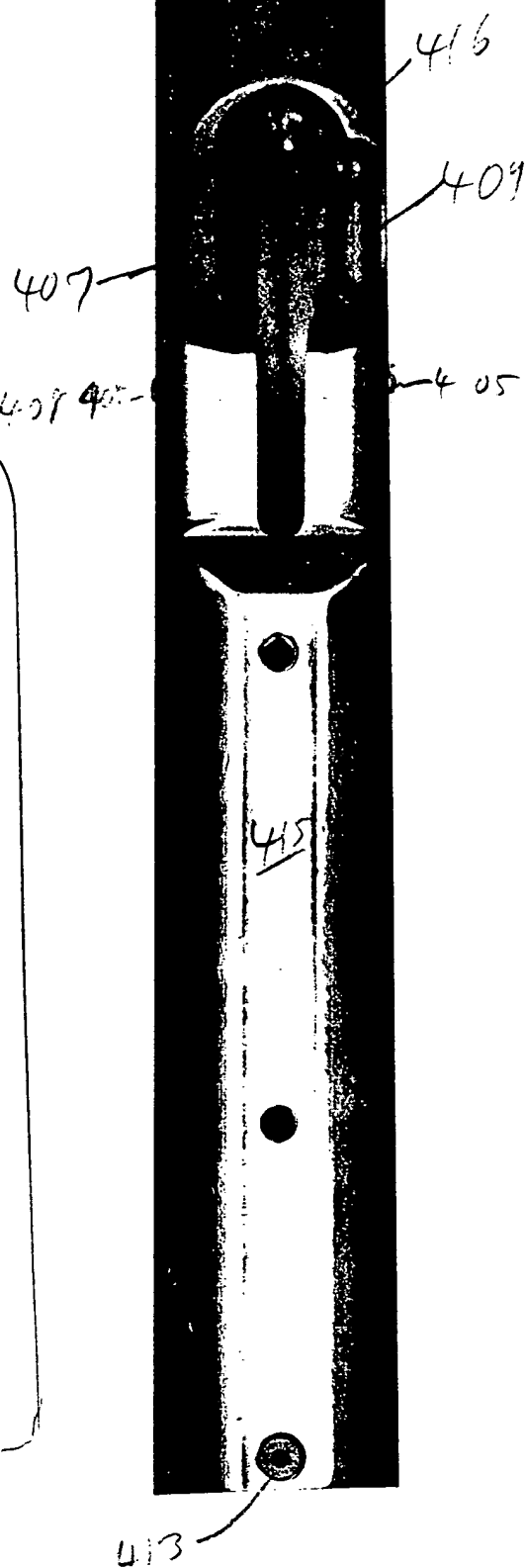


FIG 6F

408

411

420

FIL II



472

405

445

40

416

409

410

-422

-80-

FIG 11 R

FIG. 11 E

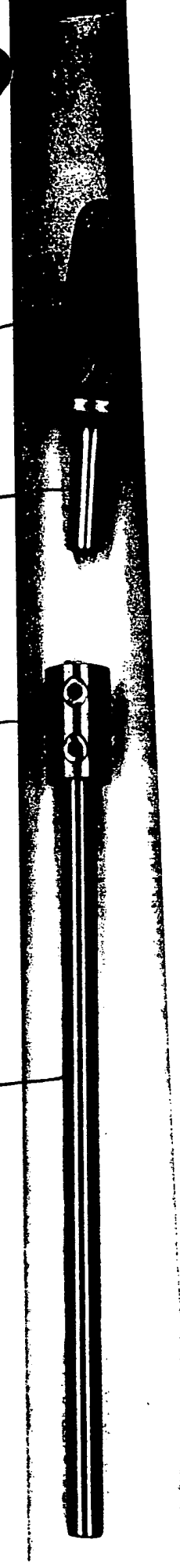
302

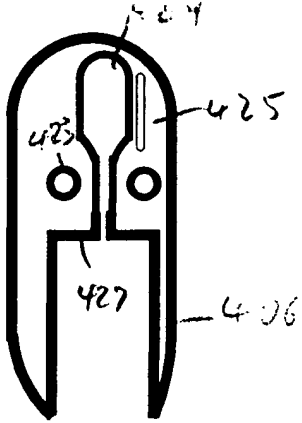
303

311

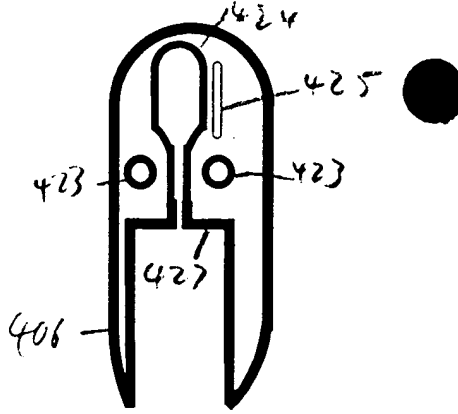
313

FIG 11 E

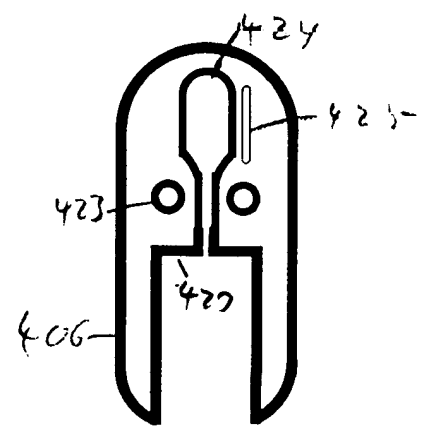




A



B



C

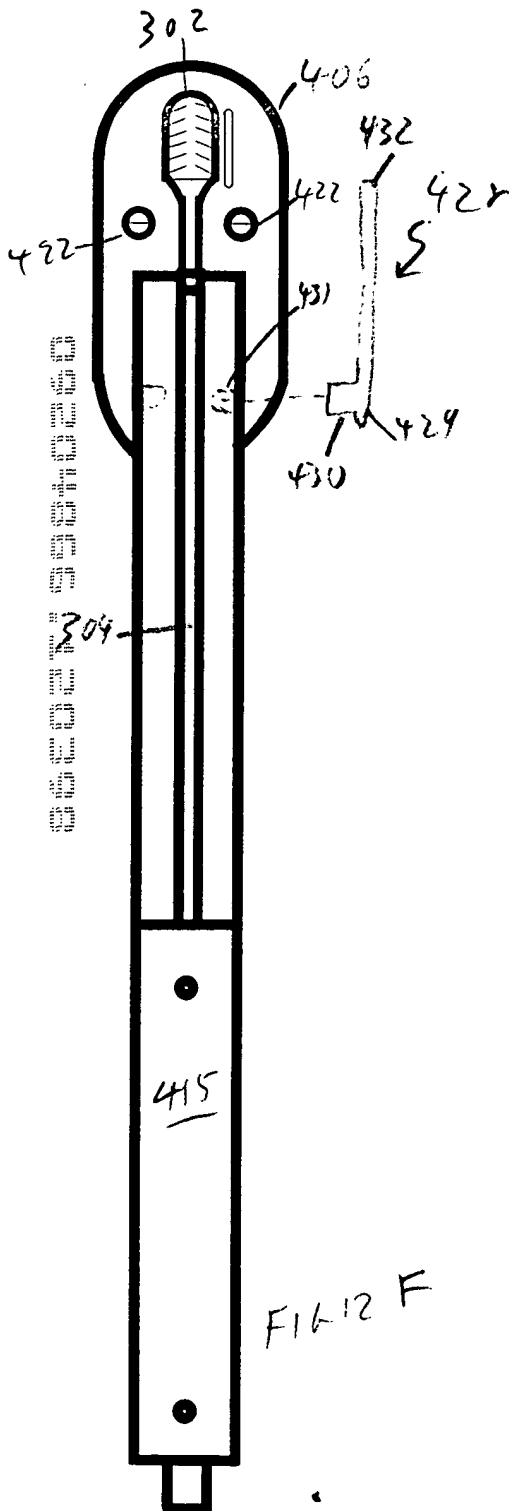


FIG 12 F

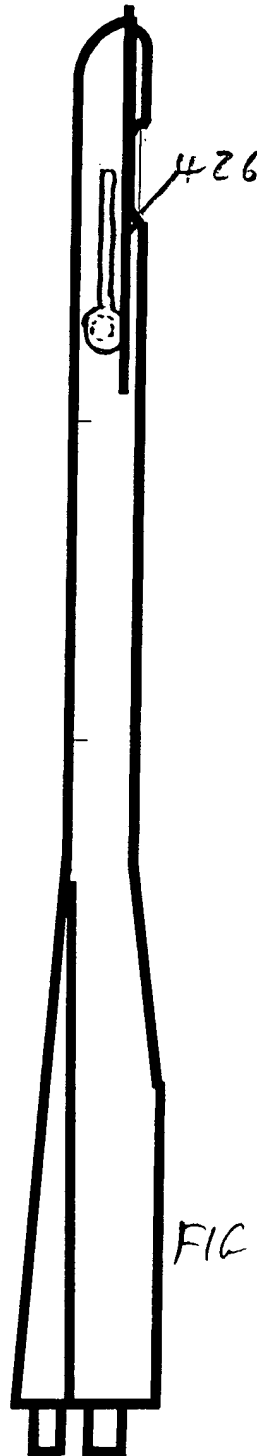


FIG 12 S

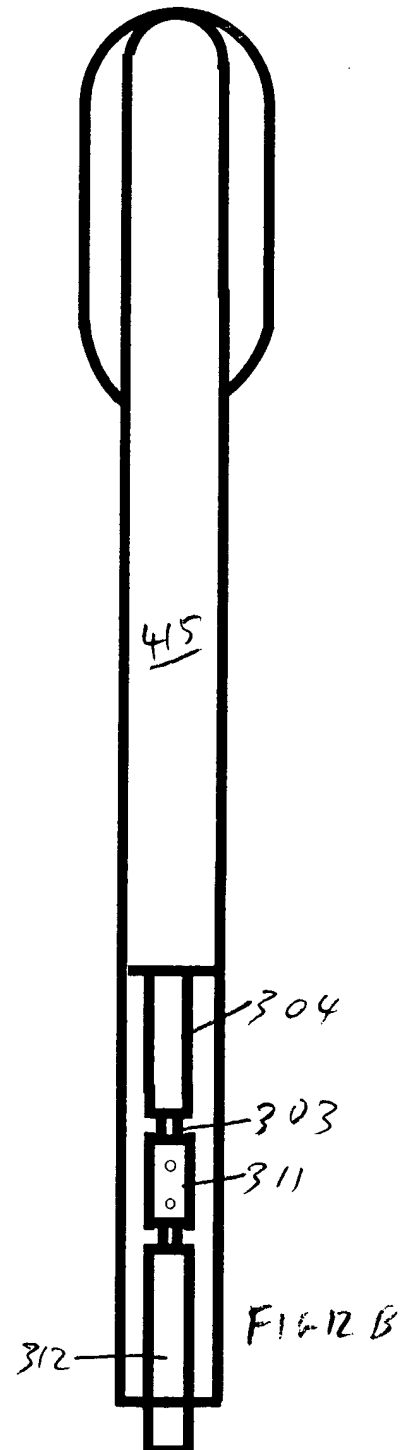


FIG 12 B

fig 19

429

FIG 14B

303

415

FIG 14A

430

415

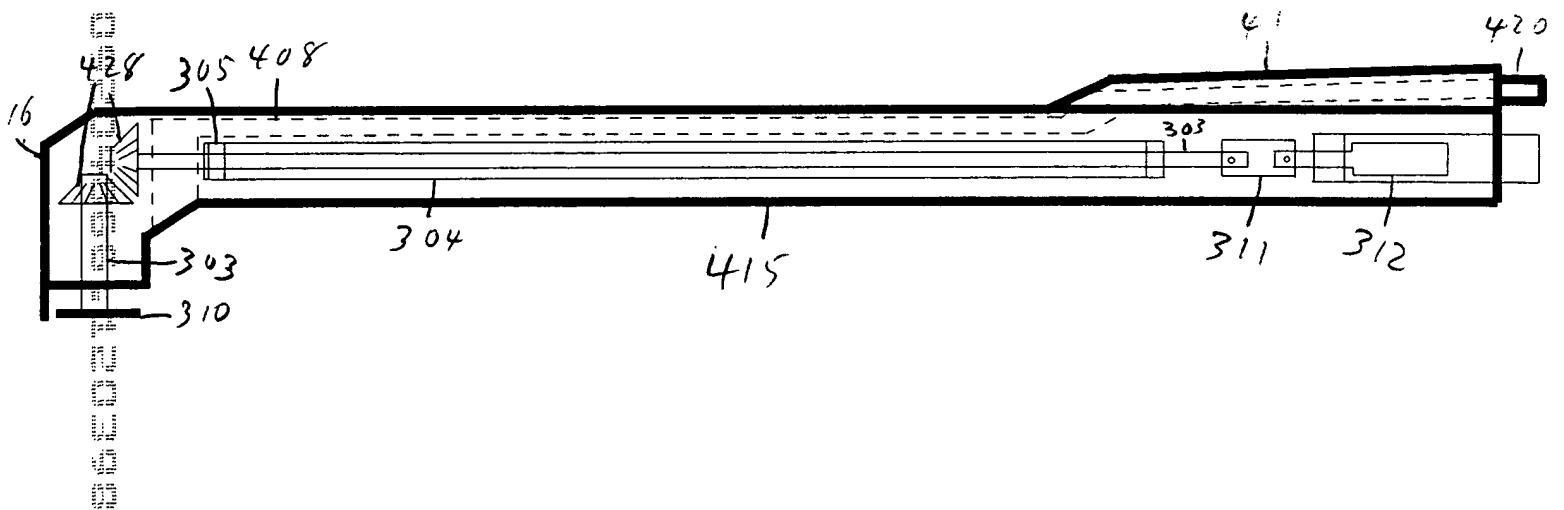
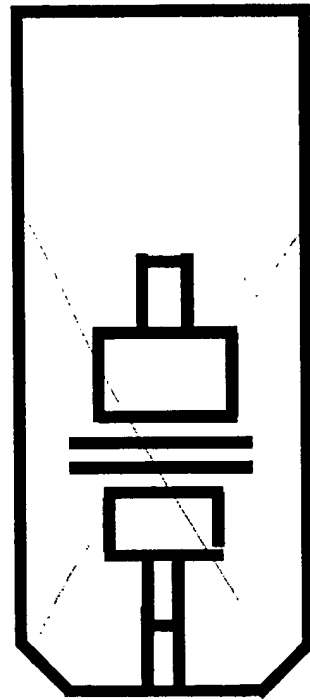
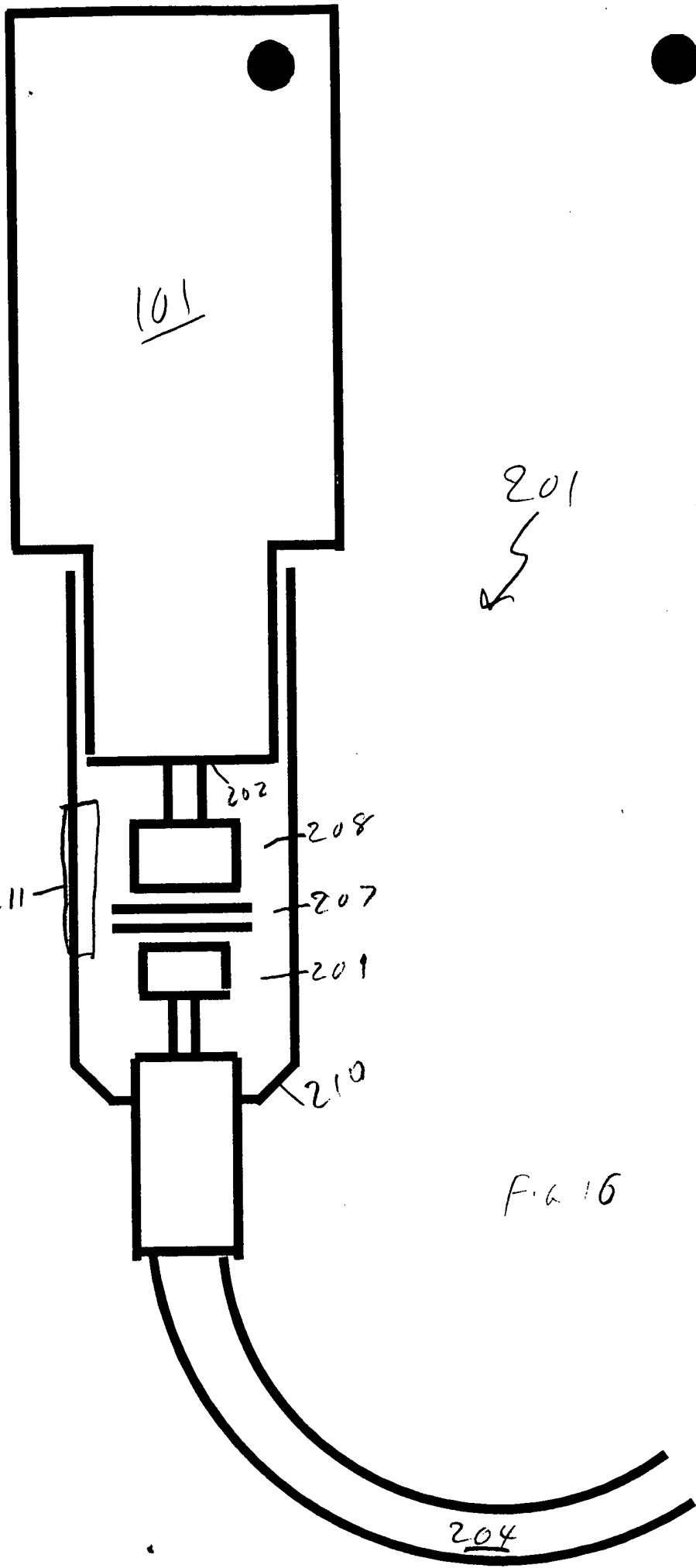


Fig 1.5

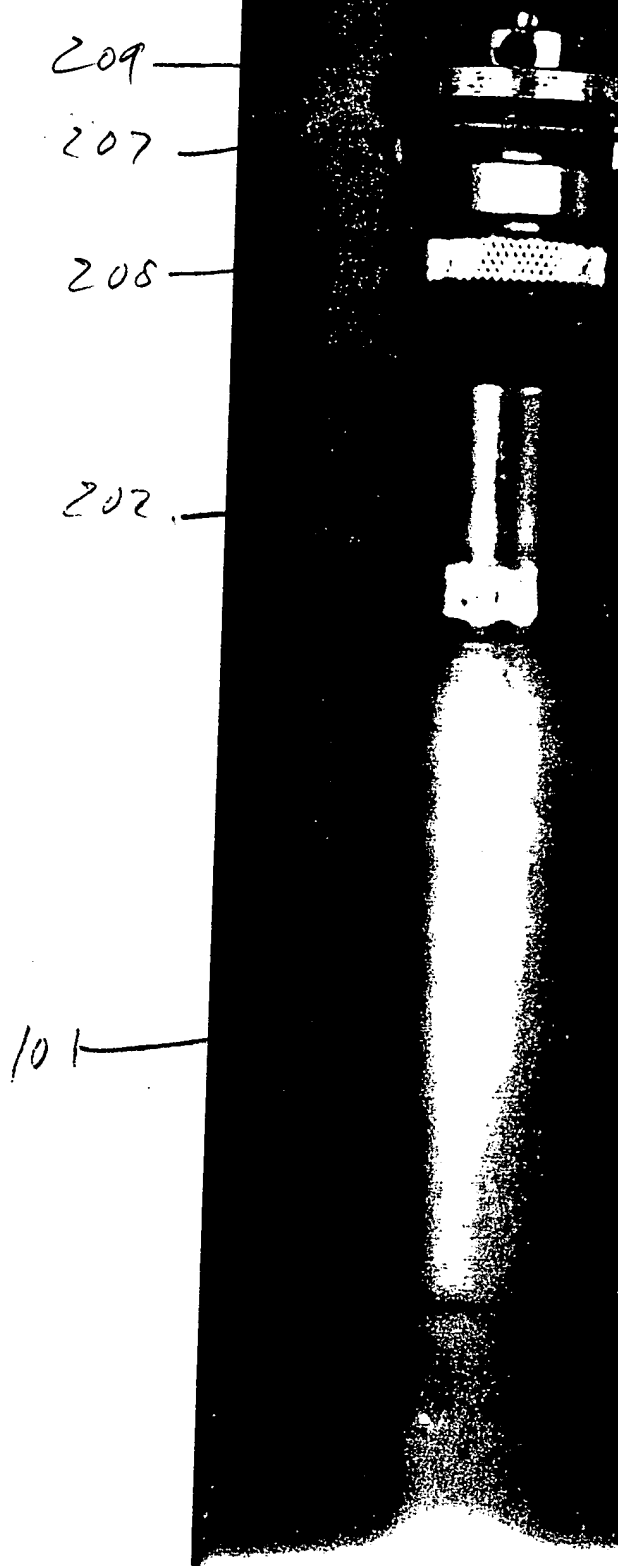
FIG. 16 is a schematic diagram of a device 101, showing a cross-sectional view of the device. The device includes a housing 101, a central component 202, a component 208, a component 207, a component 209, and a component 210. A curved line 204 is shown at the bottom. A dashed line 211 is shown on the left side. A dashed line 201 is shown on the right side.



F. 6 16

[illegible]

Fig 17



[illegible]

212—

213—

204_

F. 16 18